

# **Pulsed-assisted escape from zero voltage state in Josephson systems**

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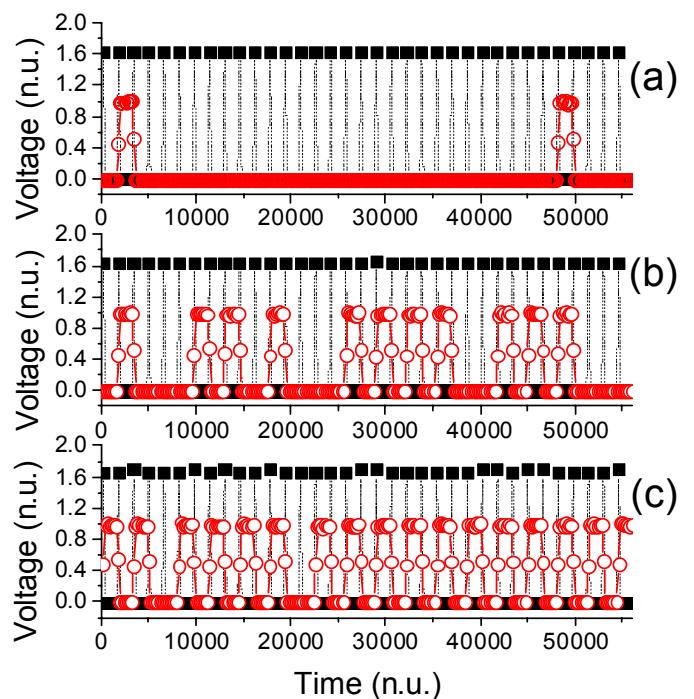
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## Non Equilibrium phenomena

Non-equilibrium phenomena in Josephson junctions are among the most promising candidates for novel applications of superconducting electronics. Transistor-like devices [1], integration with fast optoelectronic devices [2,3], and finally bi-stable devices [4] have been recently developed.

In this poster we present numerical results on the behavior of a current biased Josephson Junction (JJ) under the influence of an electronic current pulse injected both directly in the junction via its bias current and from a second JJ forming a double tunnel junction stacked device.

We study the passage from the zero voltage state to the dissipative state produced by injected electronic pulses which have also the possibility of resetting, i.e., the pulse assisted return to zero voltage.

## Two configurations studied

Single junction:

$$-\partial_{xx}\varphi + \partial_{tt}\varphi + \alpha\partial_t\varphi + \sin\varphi = \gamma_B + \gamma_N(x,t) + \gamma_P(x,t)$$

Stacked junctions [4]:

$$\varepsilon\partial_{xx}\varphi - \partial_{xx}\psi + \partial_{tt}\psi + \alpha\partial_t\psi + \sin\psi + \gamma_P(x,t) + \gamma_N(x,t) = 0$$

$$\varepsilon\partial_{xx}\psi - \partial_{xx}\varphi + \partial_{tt}\varphi + \alpha\partial_t\varphi + \sin\varphi + \gamma_B + \gamma_N(x,t) = 0$$

With times normalized to  $\omega_J$ , the plasma frequency and lengths to Josephson length  $\lambda_J$ ,  $\alpha$  the normalized losses,  $\gamma_B$  a fixed bias current,  $\gamma_N(x,t)$  a white Gaussian noise at 4.2 K(cf. ref.[5])

## Pulse Modeling

$$\gamma_P(x,t) = \begin{cases} 2\Gamma\left(\frac{x}{l}\right)\left(\frac{t}{T}\right) & \text{for } 0 \leq t \leq \frac{T}{2} \\ 2\Gamma\left(\frac{x}{l}\right)\left(1 - \frac{t}{T}\right) & \text{for } \frac{T}{2} \leq t \leq T \\ 0 & \text{for } T \leq t \leq T_p \end{cases}$$

The term  $\gamma_P(x,t)$  corresponds to a pulse train of triangular pulses in time and linear in space along the Junctions.  
For longest junctions the linear space shape is substituted by a peaked function near one of junction boundaries.

## Escape rate evaluation

Simulations also show that increasing the temperature will smear the set on of transition to resistive state in bias current in both single and stacked configurations. This is very reminiscent of thermal escape from zero voltage in small or long junctions [5,9].

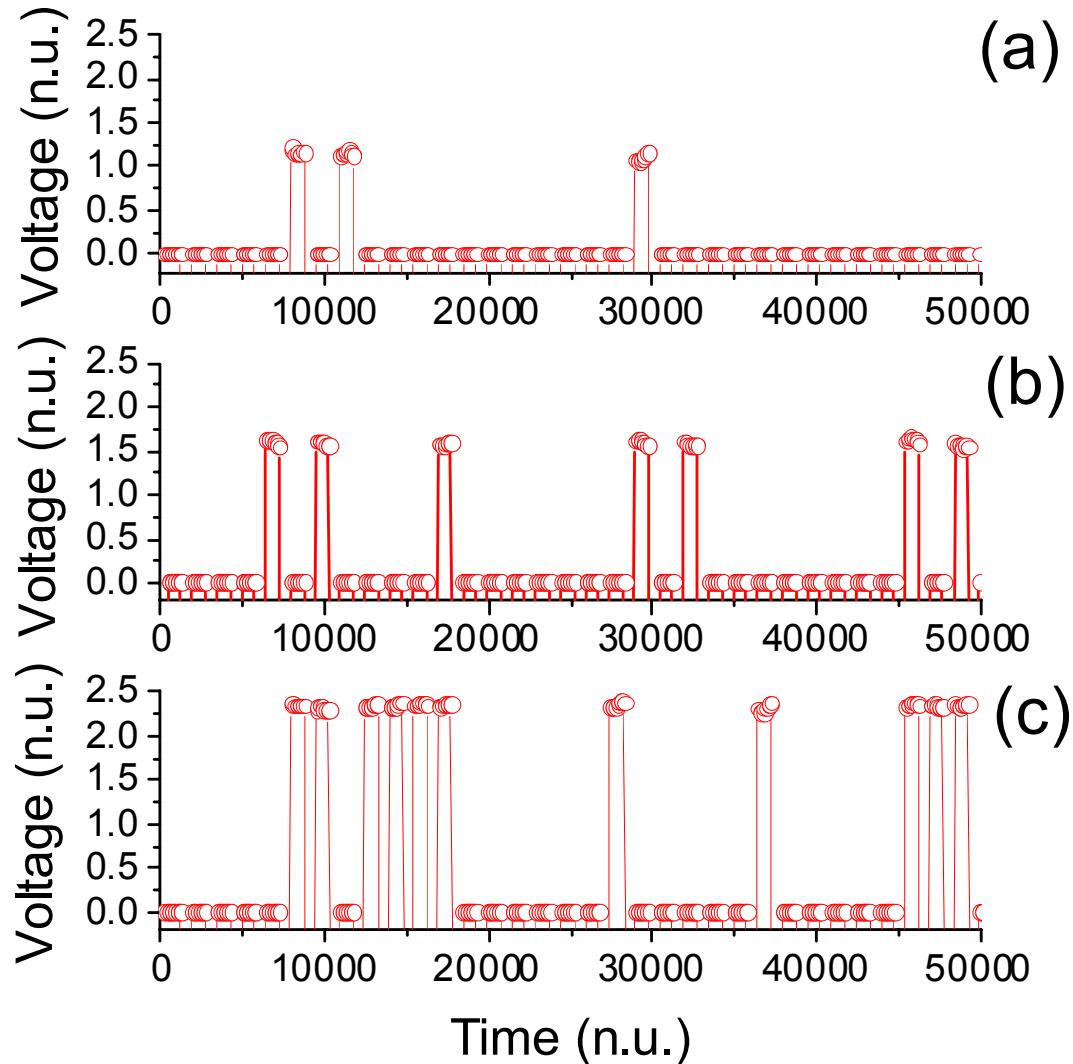
Pulses increase the energy of the Josephson oscillations causing a thermal escape toward resistive state.

If this is true the escape rate would depends exponentially on bias current and temperature, i.e.,

$$\Pi = (\omega_J / 2\pi) \exp(-U_0(\gamma - \gamma_0)^{3/2} / kT)$$

Here  $U_0$  and  $\gamma_0$  are free parameters to be fitted  
Escape rate is simply the inverse mean lifetime  $P=1/\langle\tau\rangle$  of zero voltage state

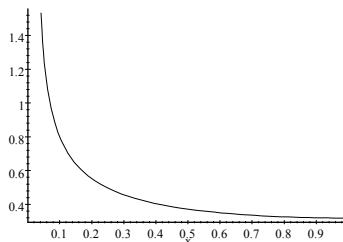
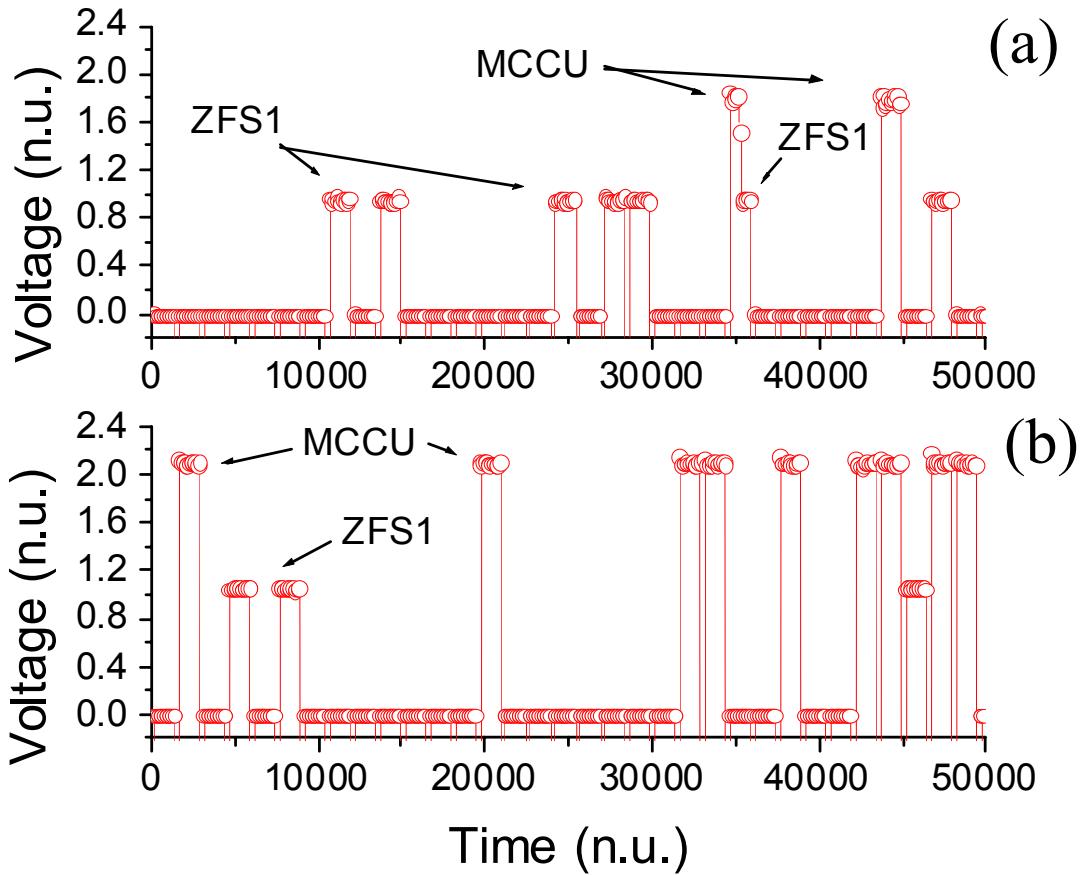
## Single junction pulsed operation



$I=1.0$   
 $T=4.2 \text{ K}$   
 $I_0=1 \text{ mA}$   
 $\alpha_0=0.15$   
 $T=100 \text{ n.u.}$   
 $T_p=1500 \text{ n.u.}$   
 $\Gamma=-20$

(a)  $\gamma_B=0.21$   
(b)  $\gamma_B=0.25$   
(c)  $\gamma_B=0.35$

# Single junction: Long Junction case

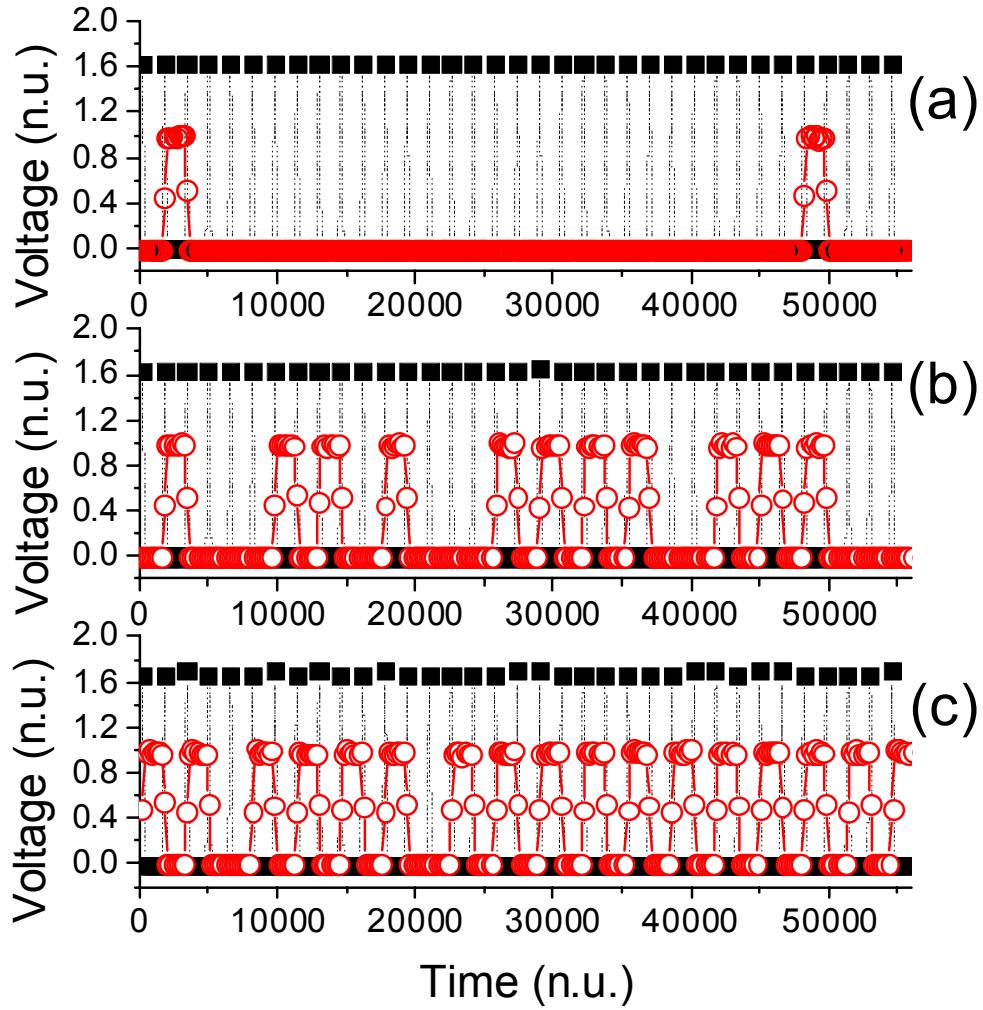


Used  
peaked  
pulse  
cf.  
ref.[6]

$T=4.2 \text{ K}$   
 $I_0=1 \text{ mA}$   
 $\alpha_0=0.15$   
 $T=100 \text{ n.u.}$   
 $T_p=1500 \text{ n.u.}$   
 $\Gamma=-20$

(a)  $\gamma_B=0.28, l=5.0$   
(b)  $\gamma_B=0.35, l=5.0$

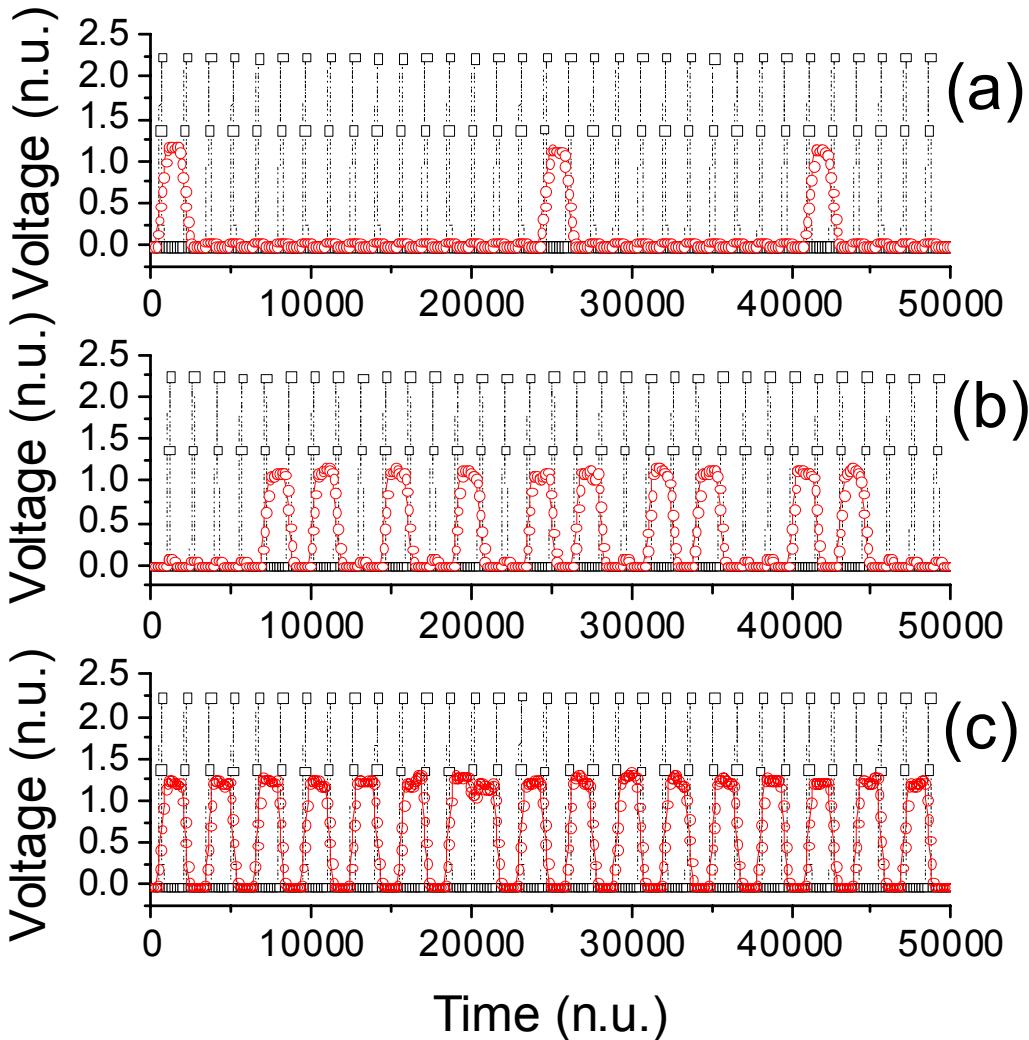
# Stacked junctions configuration (pulse amplitude variation)



$l=1.0$   
 $\varepsilon=-0.85$   
 $T=4.2 \text{ K}$   
 $I_0=1 \text{ mA}$   
 $\alpha_0=0.15$   
 $T=100 \text{ n.u.}$   
 $T_p=1500 \text{ n.u.}$   
 $\gamma_B=0.20$

(a)  $\Gamma=20.4$   
(b)  $\Gamma=20.6$   
(c)  $\Gamma=21.0$

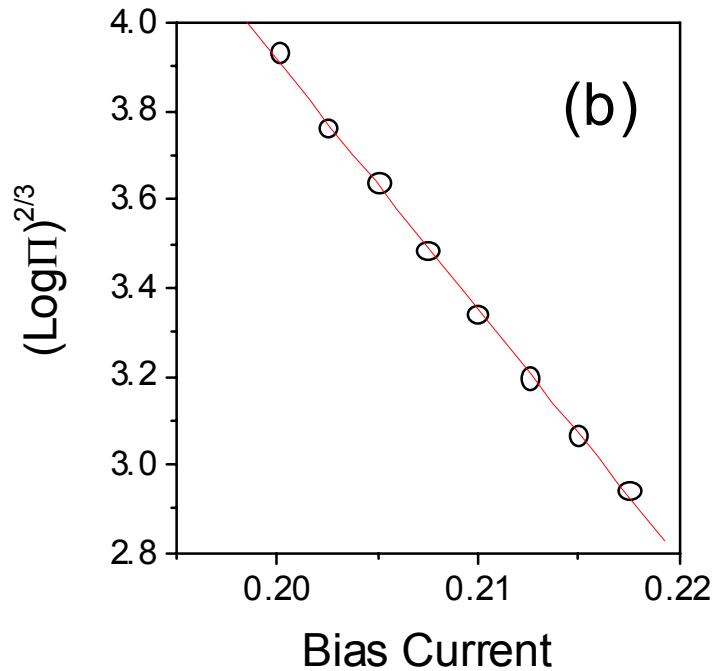
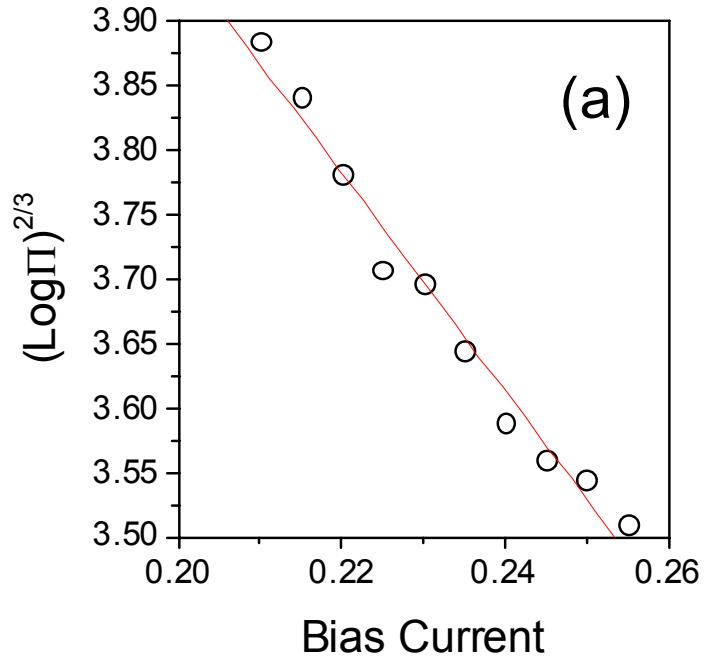
## Stacked junctions configuration



$l=1.0$   
 $\varepsilon=-0.85$   
 $T=4.2 \text{ K}$   
 $I_0=1 \text{ mA}$   
 $\alpha_0=0.15$   
 $T=100 \text{ n.u.}$   
 $T_p=1500 \text{ n.u.}$   
 $\Gamma=20$

(a)  $\gamma_B=0.205$   
(b)  $\gamma_B=0.21$   
(c)  $\gamma_B=0.22$

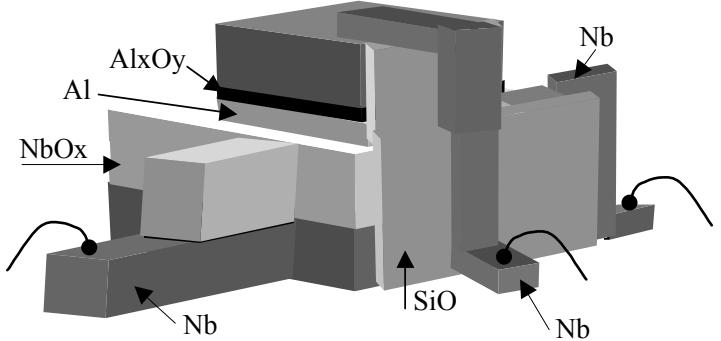
# Fit of escape rates for both single and stacked cases



Single junction with  $l=1$   
 $\gamma_0=0.6$   
 $U_0=0.2 \text{ \% J.Energy}$

Stacked junctions  $l=1$   
 $\gamma_0=0.267$   
 $U_0=2 \text{ \% J.Energy}$

## Experiment on a stacked device



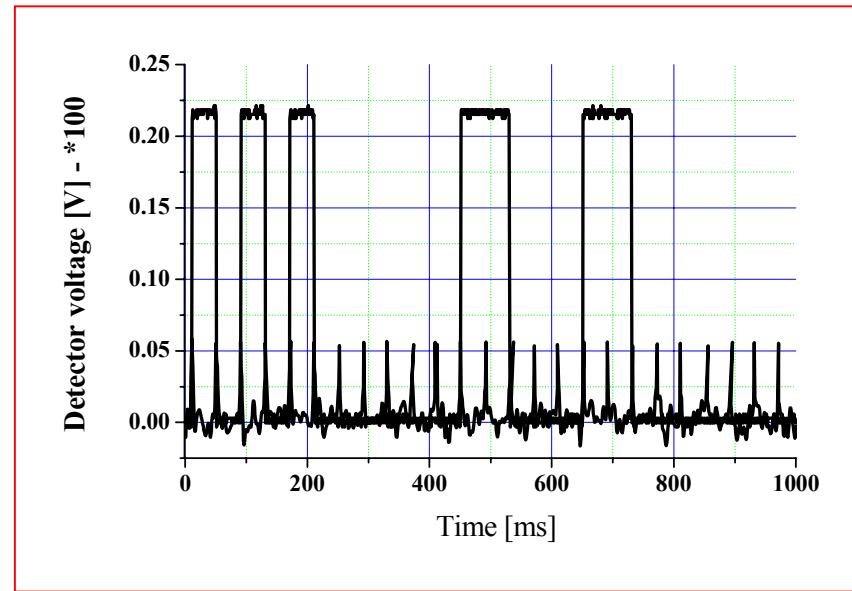
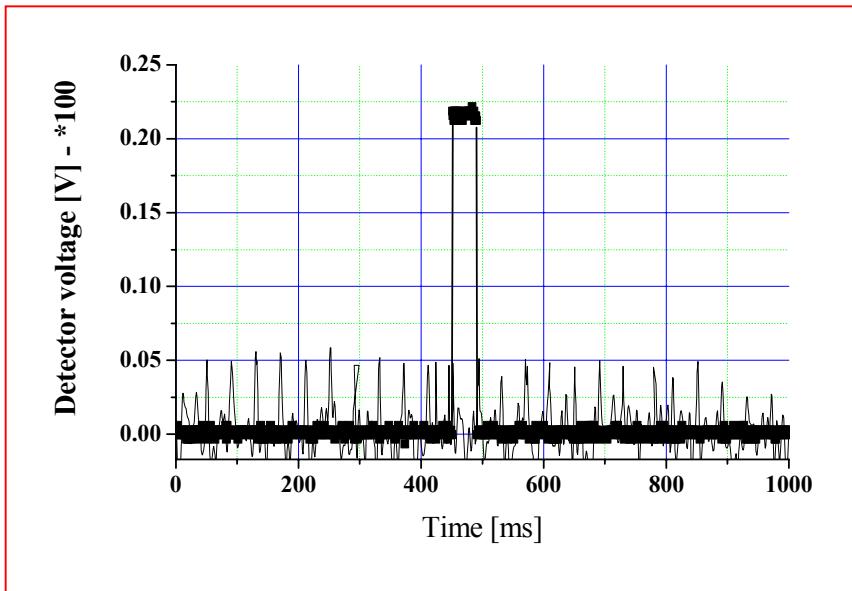
Stacked tunnel JJs have been fabricated by following a novel process developed for high-quality three terminals superconducting electronics [12]. The process and the junction characterization have been described in ref.[4].

Here  $L/ I_J \approx 1.4$ . The bottom junction was used as injector of current pulses, whose effect was observed across the top junction (detector). Injected pulses rise-times of 2 ns, 600 ms of duration, delay time 20 ms (pulse generator EG&G Mod. 480)

Each pulse was split and sent simultaneously to both the injector junction and to a digital oscilloscope (Le Croy Mod. 9361, 300MH) for triggering the waveforms acquisition.

With the detector biased at  $I < I_c$ , the output voltage across the detector was measured by a standard PAR Mod. 5113 pre-amplifier (300 kHz BW).

## Experimental result on the stacked system



The detector junction output voltage as a function of injected electronic pulses across the coupled junction for increasing pulse amplitudes. The scale is referred to the detector output voltage, while pulses are reported in arbitrary units.

## Conclusions

We presented numerical simulations and measurements of pulse activated transitions from the metastable to the running state and *viceversa* in both single junctions and stacked configuration Josephson systems.

At least for the stacked configuration where the fitting is much realistic, an interval of bias currents exists (from about 0.2 to 0.235) where the numerical escape rate can be fitted by the simple short junction theoretical thermal escape rate. Experiments on stacked device compare favorable with numerical simulations of PSGE model applied to a stacked system.

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